ECS Configuration Change Request

Page 1 of ___ Page(s)

1. Originator Brian Bresina	2. Log Date: **3/26/02**	3. CCR #: **02-0253**	4. Rev: —	5. Tel: 757-864-2108	6. Rm #: 2303	7. Dept. SAIC

8. CCR Title: CPU swap between (old sparc20 l0mos07, old sparc20 l0mos02) and l0css02.

9. Originator Signature/Date **Brian Bresina /s/ 3/26/02**	10. Class **II**	11. Type: **CCR**	12. Need Date: 021402

13. Office Manager Signature/Date **G. G. Gavigan, Jr. /s/ 3/26/02**	14. Category of Change: Initial ECS Baseline Doc.	15. Priority: (If "Emergency" fill in Block 27). Routine

16. Documentation/Drawings Impacted: 920-TDL-001 LaRC Hardware Diagram	17. Schedule Impact:	18. CI(s) Affected:

19. Release Affected by this Change: 6A	20. Date due to Customer:	21. Estimated Cost: None - Under 100K

22. Source Reference: ☐NCR (attach) ☐Action Item ☐Tech Ref. ☐GSFC ☐Other:
LRC000000004434

23. Problem: (use additional Sheets if necessary)
l0css02 needs more computing power as a DNS, NIS, DCE master because the system is becoming unresponsive due to load. A CPU swap between (old sparc20 l0mos07, old sparc20 l0mos02) and l0css02 will be done during LaRC maintenance.

24. Proposed Solution: (use additional sheets if necessary)
Swap 150 mhz CPU from old l0mos02 (ein 00004464 serial # 626f123f) into l0css02 (ein 00000309 serial # 550f39f8).
Swap 150 mhz CPU from old l0mos07 (ein 00002283 serial # 707f11A0) into l0css02 (ein 00000309 serial # 550f39f8).
Swap 75 mhz CPU from l0css02 (ein 00000309 serial # 550f39f8) into old l0mos07 (ein 00002283 serial # 707f11A0).
Swap 75 mhz CPU from l0css02 (ein 00000309 serial # 550f39f8) into old l0mos02 (ein 00004464 serial # 626f123f).
The serial numbers of the CPUs are as follows: see next page

25. Alternate Solution: (use additional sheets if necessary)

26. Consequences if Change(s) are not approved: (use additional sheets if necessary)

27. Justification for Emergency (If Block 15 is "Emergency"):

28. Site(s) Affected: ☐EDF ☐PVC ☐VATC ☐EDC ☐GSFC ☒LaRC ☐NSIDC ☐SMC ☐AK ☐JPL
☐EOC ☐IDG Test Cell ☐Other

29. Board Comments:	30. Work Assigned To:	31. CCR Closed Date:

32. EDF/SCDV CCB Chair (Sign/Date): **Randall J. Miller /s/ 3/27/02**	Disposition: Approved App/Com. Disapproved Withdraw Fwd/ESDIS ERB Fwd/ECS
33. M&O CCB Chair (Sign/Date): **G. G. Gavigan, Jr. /s/ 3/26/02**	Disposition: Approved App/Com. Disapproved Withdraw Fwd/ESDIS ERB Fwd/ECS
34. ECS CCB Chair (Sign/Date):	Disposition: Approved App/Com. Disapproved Withdraw Fwd/ESDIS ERB Fwd/ESDIS

CM01JA00 Revised 10/15/01   ECS/EDF/SCDV/M&O

ADDITIONAL SHEET

CCR #: 02-0253   Rev: – Originator:

Telephone: Office:

Title of Change: CPU swap between (old sparc20 l0mos07, old sparc20 l0mos02) and l0css02.

SNs of the CPUs PULLED from l0mos02 and l0mos07 (both SPARC 20s) and PLACED
INTO l0css02

3702162011560
3702162044256

SNs of the CPUs PULLED from l0css02 and PLACED INTO l0mos02 and l0mos07

501300190021857 (into l0mos07)
501300190021735 (into l0mos02)

CM01AJA00 Revised 10/15/01   *ECS*